

Saipanmuluk Mujawar

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Career Objective

To have a growth-oriented and challenging career, where I can have the twin benefits of contributing to the company with my knowledge and skill as well as professional growth and experience through continuous learning.

Work Experience

Leistung Core India pvt ltd Pune.

Department :- Research & Development.
Designation :- Firmware Embedded Engineer.
Period :- Aug-2024 to Present.

➤ **Rolls and Responsibilities: -**

○ **Embedded System Development – Vibration Control Project:**

- Worked on the **Vibration Control Project**, utilizing peripherals like PWM, ADC & UART.
- Interfaced with 7-segment LEDs, buttons, and integrated 5-6 sensors for data acquisition and handling. Implemented real-time data processing and ensured seamless communication across interfaces.
- Optimized system performance through robust firmware design in C/Embedded C.

○ **PCB Design**

- Proficient in PCB design with expertise in creating component libraries and generating Gerber files.
- Created optimized PCB layouts, component libraries, and generated Gerber files for production

Vendekin Technology pvt ltd Pune.

Department :- Research & Development.
Designation :- Intern Embedded Engineer.
Period :- Feb-2024 to Aug-2024

➤ **Rolls and Responsibilities: -**

○ **QR Code Display System Development:**

- Developed and implemented a QR code display system using ESP32 microcontroller and 2.4" TFT SPI Display.
- Designed system architecture, wrote firmware in C/C++ for QR code generation.

○ **PCB Design and Hardware Testing**

- Proficient in PCB design with expertise in creating component libraries and generating Gerber files.
- Skilled in using software like Eagle, Altium and KICAD to develop and optimize PCB layouts.
- Knowledge of hardware testing utilizing Digital Storage Oscilloscope (DSO) & Multimeter.

Software & Hardware Skill

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|--------------------------------|--|
| <i>Operating System</i> | <i>Windows</i> |
| <i>Microcontroller</i> | <i>PIC-18F4520, ARM7, STM32, ESP32.</i> |
| <i>Programming Language</i> | <i>C, C++, Embedded C.</i> |
| <i>Development Environment</i> | <i>MPLAB X IDE, Proteus, Keil uVision4, Aurdino IDE.</i> |
| <i>Communication Protocol</i> | <i>UART, I2C, SPI, CAN</i> |

Education & Course Details

| Name of Course | Name of Institute | University Board | Grade/Percentage |
|---------------------------------------|---------------------------------|-------------------------|-------------------------|
| P.G Diploma in Embedded System Design | BICARD Pune | - | A grade |
| BE | Zeal college of engg. Pune. | SPPU Pune | 7.22 |
| HSC | C.B. Khedgis college, Akkalkot | Maharashtra state board | 68.15 |
| SSC | PPMV High School Kondhawa, Pune | Maharashtra state board | 77.2 |

Personal Details

Date of Birth :- 01-06-1998
Gender :- Male
Marital Status :- Unmarried.
Address :- At/Post- Solapur.
Languages Known :- English, Hindi, Marathi, Kannada

Declaration

I, hereby declare that the above-mentioned facts and details are true to the best of my knowledge and belief.

Place: Pune

Signature
(Saipanmuluk Mujawar)